AMENDMENTS TO THE CLAIMS

1-32. (Canceled)

- 33. (Currently Amended) A packaged electronic device comprising:
- an electronic device;
- a first container member configured to mount the electronic device thereon;
- a second container member configured to form a space for housing said electronic device in cooperation eorporation with said first container member; and

a metal layer configured to close said space by bonding said first container member and said second container member,

said metal layer being formed by radiating an energy wave to surfaces of a first metal part of said first container member and a second metal part of said second container member while said first metal part and said second metal part are heated at a room temperature or higher and 150°C or lower in atmospheric air, and by bringing said first metal part and said second metal part into contact with each other.

- 34. (Currently Amended) The packaged electronic device according to claim 33, wherein said metal layer <u>includes is of gold</u>.
- 35. (Previously Presented) The packaged electronic device according to claim 33, wherein said first container member or said second container member is made of resin.

36. (Currently Amended) A manufacturing method of a packaged electronic device comprising:

bonding a first container member on which an electronic device is mounted and a second container member configured to form a space for housing said electronic device in <u>cooperation</u> eorporation with said first container member with a metal layer for closing the space;

when said bonding, radiating <u>an</u> energy wave to a first metal part of said first container member and a second metal part of said second container member while said first metal part and said second metal part are heated at a room temperature or higher and 150°C or lower in atmospheric air; and

forming said metal layer by bringing said first metal part and said second metal part radiated with said energy wave into contact with each other.

37. (Currently Amended) A packaged electronic device comprising:

a container configured to form a closed inner space by a first inner face, a second inner face opposed on the opposite to said first inner face, and side faces perpendicular to said first inner face and said second inner face;

a first electronic device to be mounted on a first mounting face of said first inner face, said second inner face, or said side faces;

a second electronic device to be mounted on a second mounting face different from said first mounting face of said first inner face, <u>said</u> second inner face, or <u>said</u> side faces;

a first outer electrode formed on an outer face of said container facing to said first

mounting face and configured to be electrically connected with said first electronic device; and a second outer electrode formed on an outer face of said container facing to said second mounting face and configured to be electrically connected with said second electronic device,

said container including a recessed part; a main body member configured to mount said second electronic device on a bottom of said recessed part; a first cover member configured to be attached to an aperture of said recessed part and configured to mount said first electronic device; and a metal layer configured to close said inner space by bonding said main body member and said first cover member,

said metal layer being formed by radiating <u>an</u> energy wave to metal parts of said main body member and said first cover member while said metal parts are heated at a room temperature or higher and 150°C or lower in atmospheric air, and by bringing <u>both-said</u> radiated and heated metal parts into contact with each other.

38. (Currently Amended) A manufacturing method of a packaged electronic device comprising:

mounting a first electronic device to be connected electrically with a <u>first</u> via on a first mounting face of a cover member having the <u>first</u> via formed therein;

mounting a second electronic device to be connected electrically with a <u>second</u> via on a second mounting face of a main body member having the <u>second</u> via formed therein and forming a closed inner space in <u>cooperation eorporation</u> with said cover member; and

forming a packaged electronic device by bonding said cover member and said main body

member so as to arrange said first electronic device and said second electronic device in said inner space and thereby closing said inner space,

said bonding of said cover member and <u>said</u> main body member being carried out by:

radiating <u>an</u> energy wave to a first metal part formed on said first mounting face of said

cover member and a second metal part formed on said main body member while said first metal

part and said second metal part are heated at a room temperature or higher and 150°C or lower in

atmospheric air; and

thereafter, forming said packaged electronic device by bonding said cover member and said main body member with said first metal part and said second metal part connected and closing said inner space.

- 39. (Currently Amended) The manufacturing method of a packaged electronic device according to claim 38, wherein said electric connection of said <u>first and second</u> vias with said first electronic device and <u>said second</u> electronic device is carried out by radiating <u>an energy</u> wave to said <u>first and second</u> vias, and electrodes of said first electronic device and <u>said second</u> electronic device; and thereafter bringing said <u>first and second</u> vias into contact with said first electronic device and <u>said second</u> electronic device.
- 40. (Currently Amended) The manufacturing method of a packaged electronic device according to claim 38, wherein said electric connection of said <u>first and second</u> vias with said first electronic device and <u>said</u> second electronic device is carried out by providing a curable anisotropic

conductive or non-conductive resin between electrodes of said first electronic device and <u>said</u> second electronic device, and said <u>first and second</u> vias.